IN THE SPECIFICATION:

Please amend the specification as follows:

Page 8, replace the fifth full paragraph with the following paragraph:

Fig. 5 shows a diagram for use in schematically describing propagation delay time tpd when input of inverter changes from low level to high level; and

Page 10, replace the third full paragraph with the following paragraph:

S_{out} is calculated by the next expression:

C_{out} (pF): load capacitance of transistor connected to output pin;

$$S_{out} = \alpha \left(\frac{C_{out}}{W_{out}} \right)^{\beta} \qquad (4)$$

α, β : constants depending on rounding of waveform of output pin; and

W_{out} (μ m): width of N-channel transistor connected to output pin.

Page 15, replace the fourth full paragraph with the following paragraph:

In the case where AC bias has an iterative waveform at a period T, the expression (13) is transformed into:

$$ageAC(t_{ac}) = \frac{t_{ac}}{T} \int_{0}^{t_{ac}} \frac{I_{ds}}{BW} \left(\frac{I_{sub}}{I_{ds}} \right)^{m} dt \qquad (14)$$